

PG301 A02

Comanche 192b GDDR5, <150W, 2-way SLI

Tall DVI-I + DP + DP + DP/HDMI + DP

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Note:

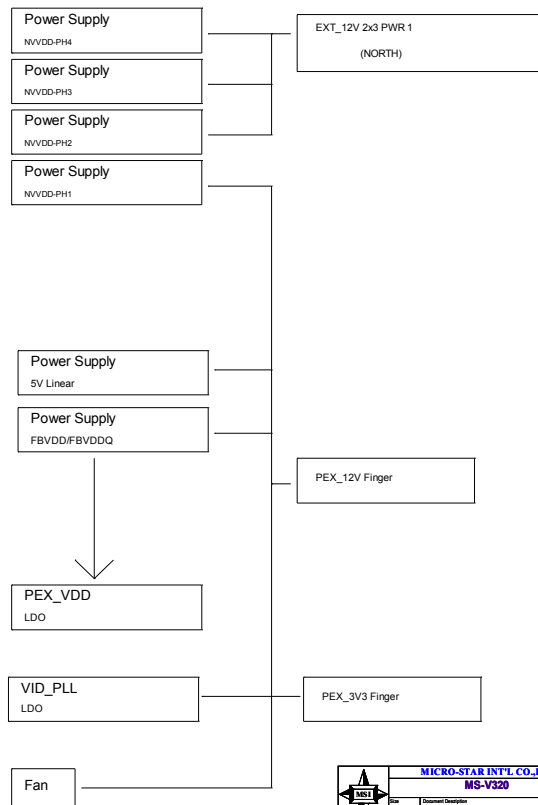
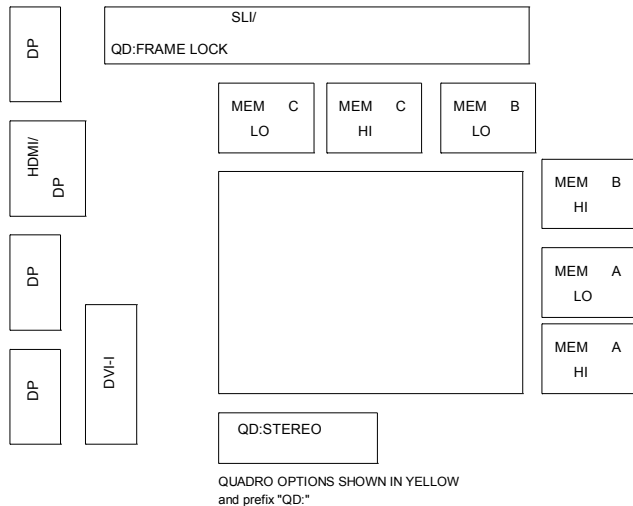
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- page:8 增加跨頁符號
- page:15 修改DVI footprint
- page:16/17/19 修改DP footprint
- page:18 修改HDMI footprint
- page:26/27 修改output chike footprint



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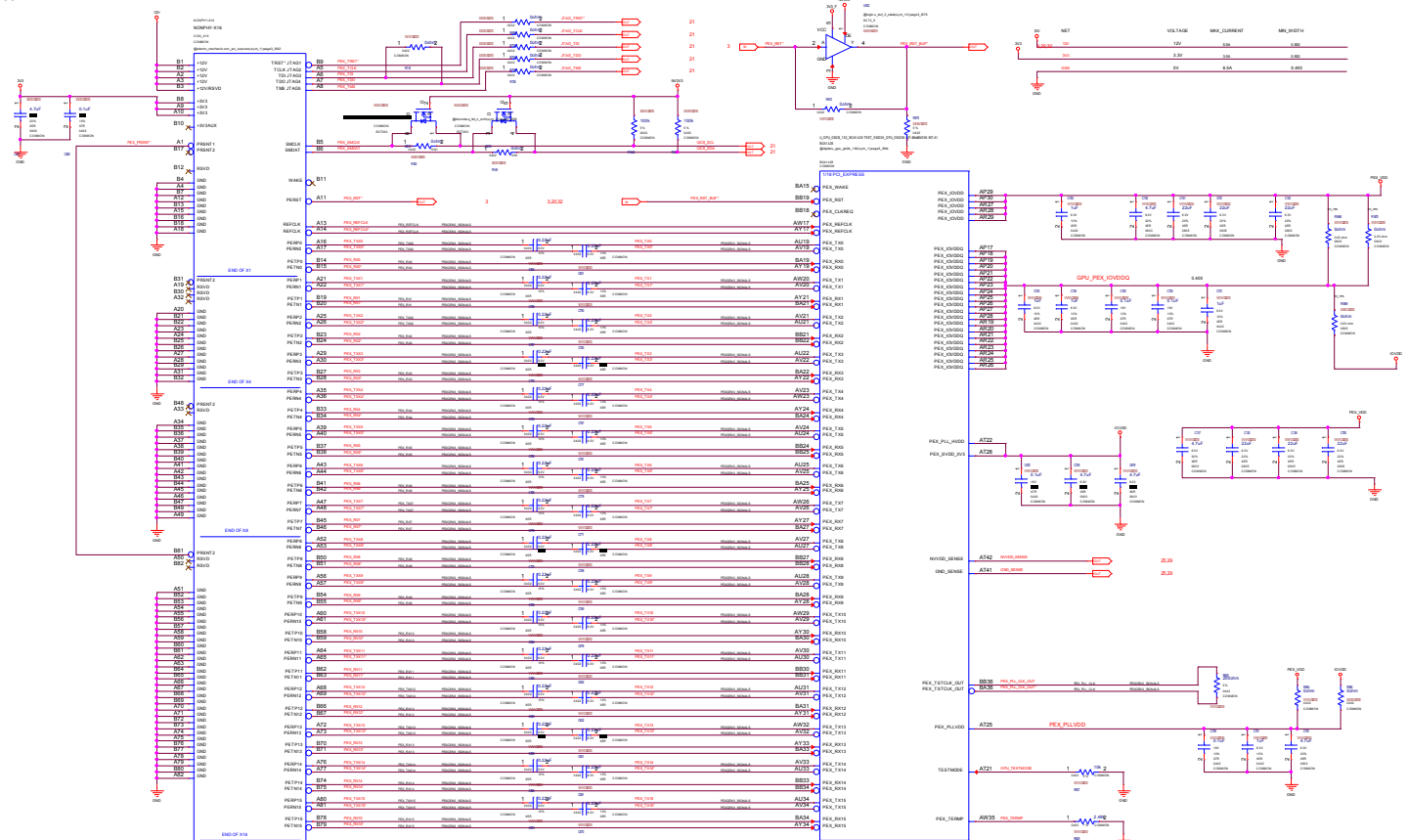
Block Diagram



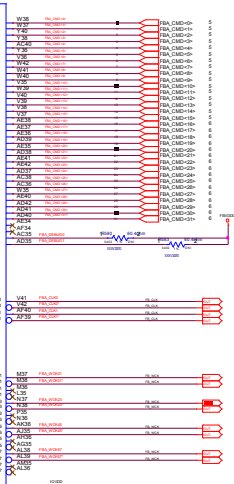
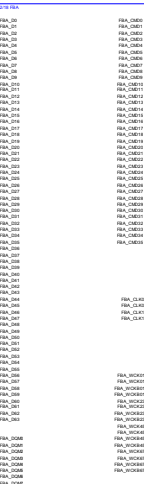
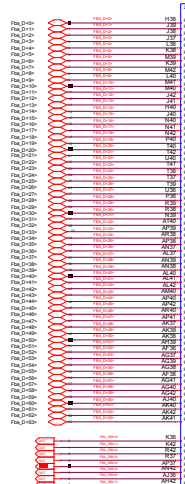
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PCI Express



U_CPL_DDR_162_B0H428 TEST_0203H_CPL_0203H BT A1_0203H BT A1_0203H



GDDR5 BGA170 MIRROR

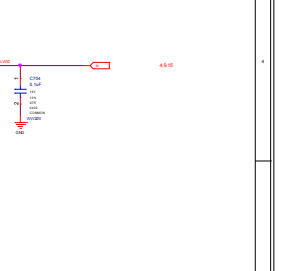
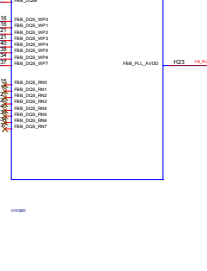
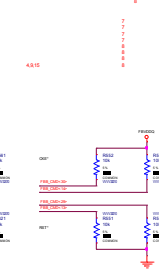
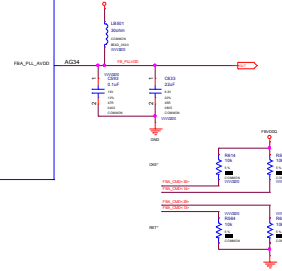
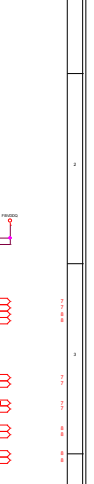
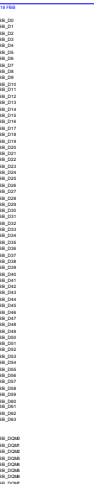
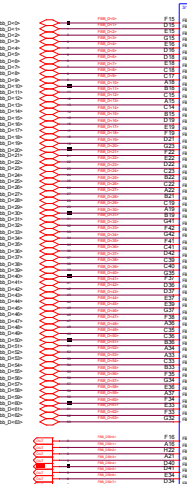
0.31 32.43

20

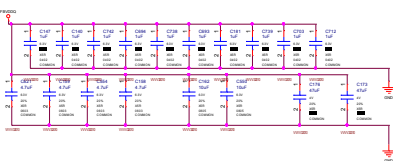
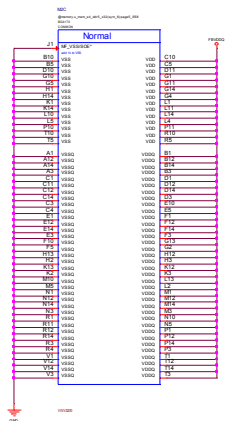
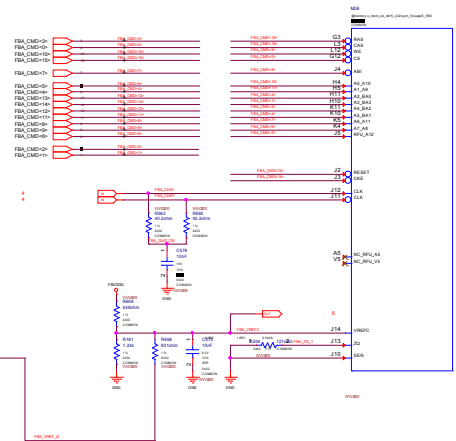
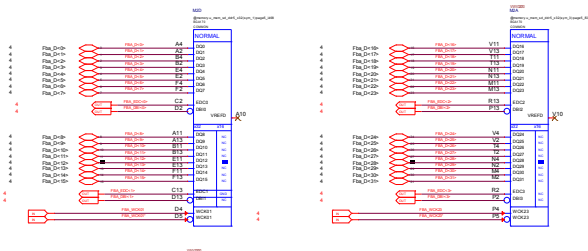
178 F608

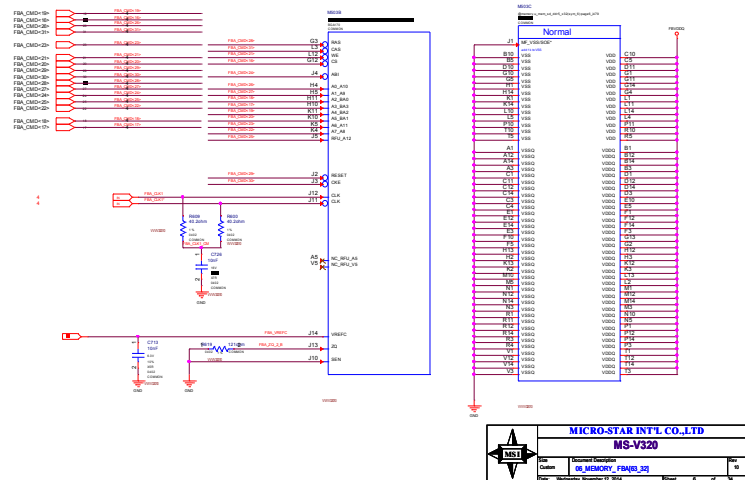
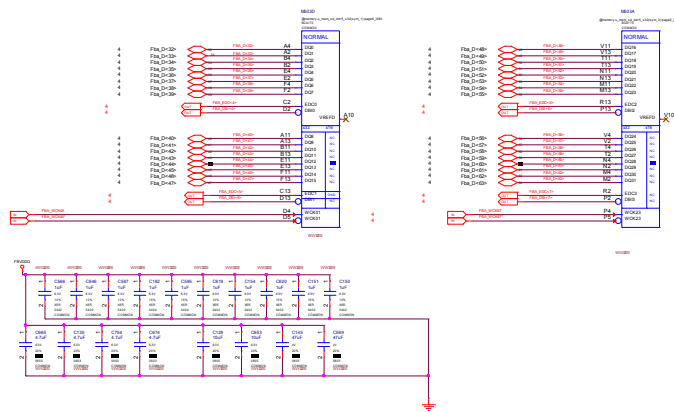
[illegible][illegible]

Q: [DPC_0028_162_001A28 TEST_DK28_DPC_DK28 AT 40-0028-07-01](#)
GNC

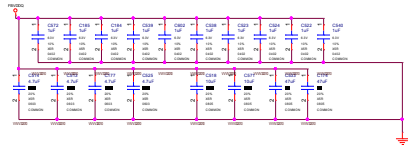
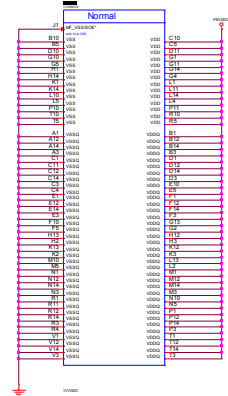
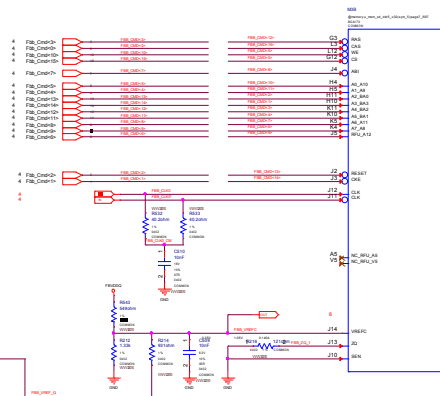
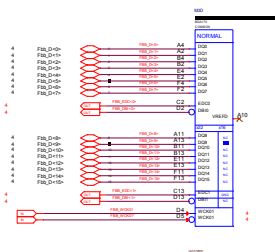


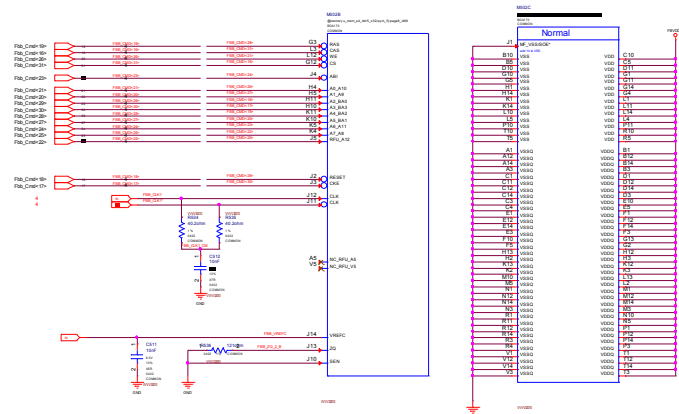
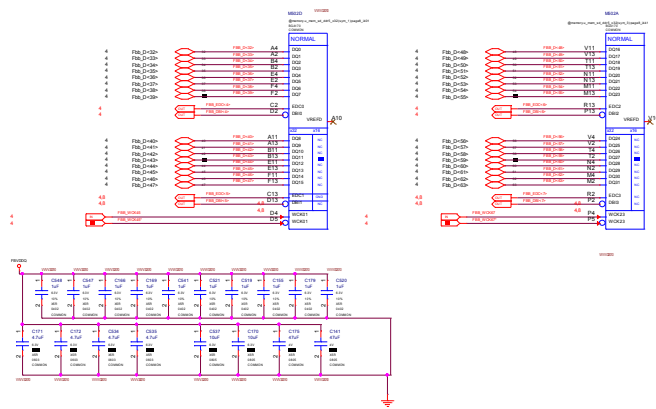
MEMORY: FBA Partition 31..0





MEMORY: FBB Partition 31..0

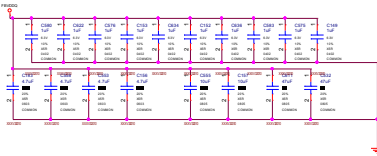
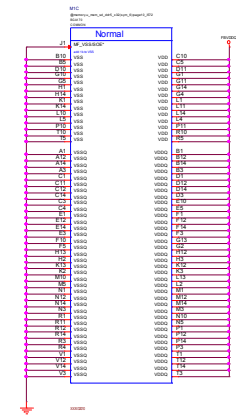
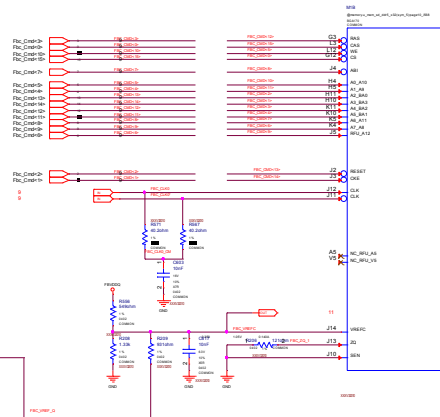
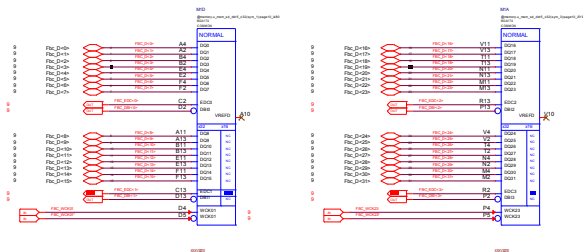




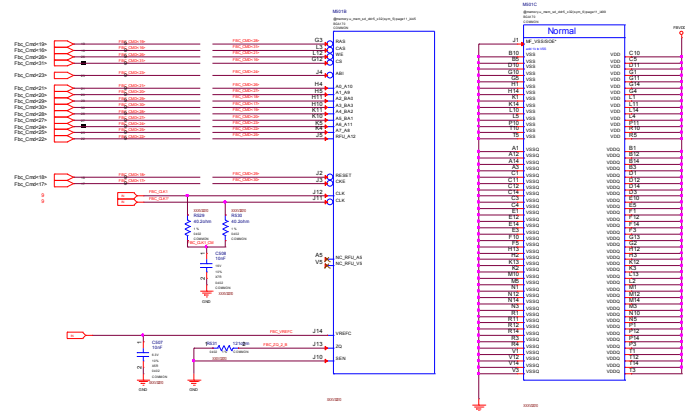
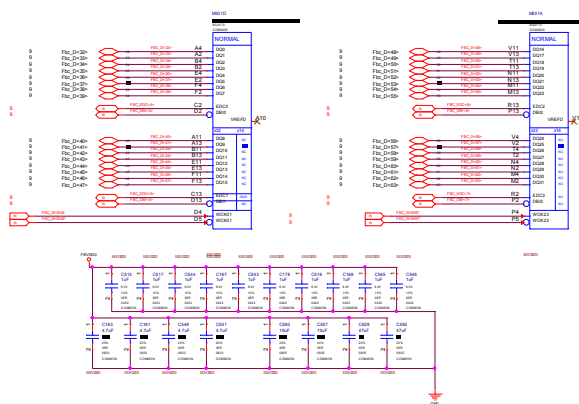
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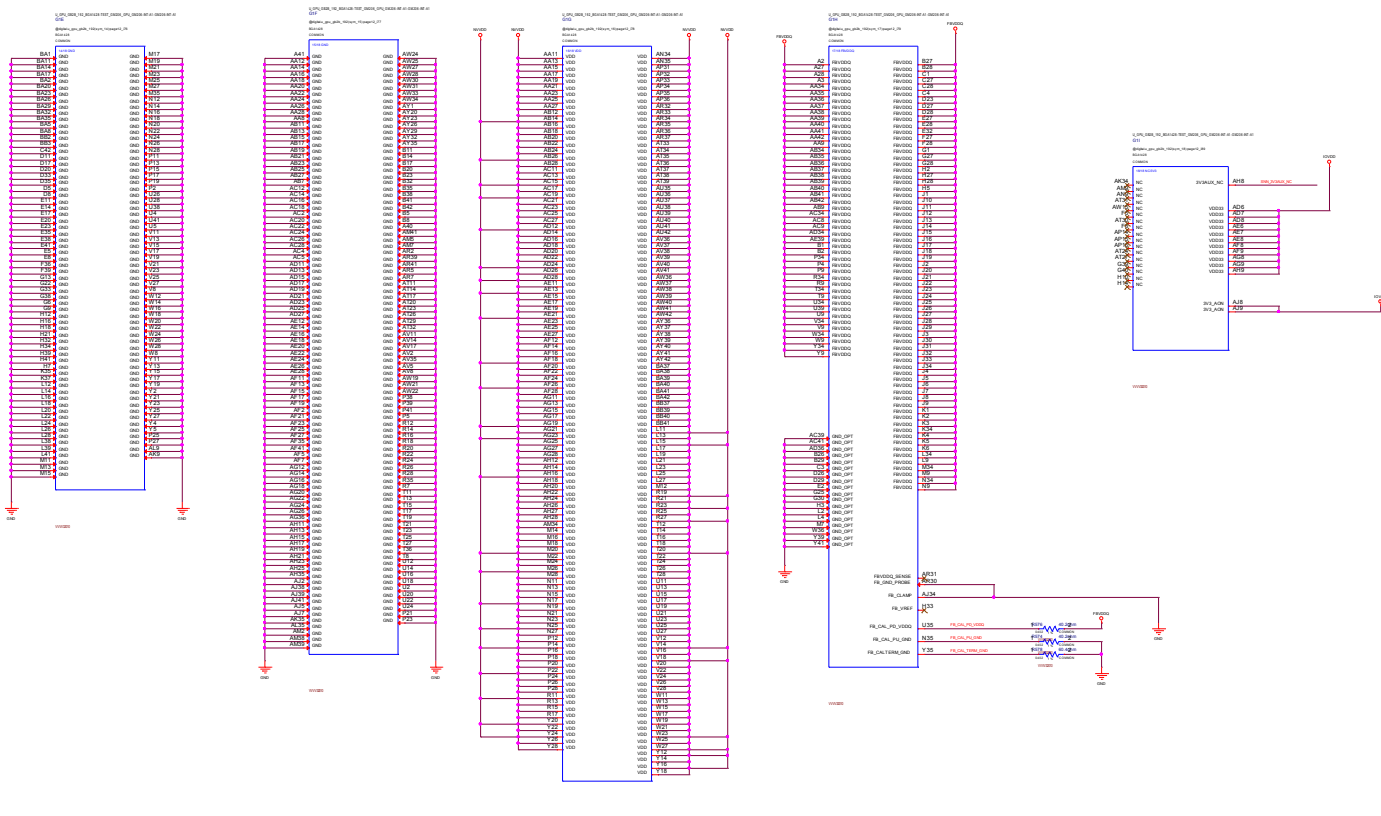
MEMORY: FBC Partition 31..0



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GPU PWR and GND



DACA Interface

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

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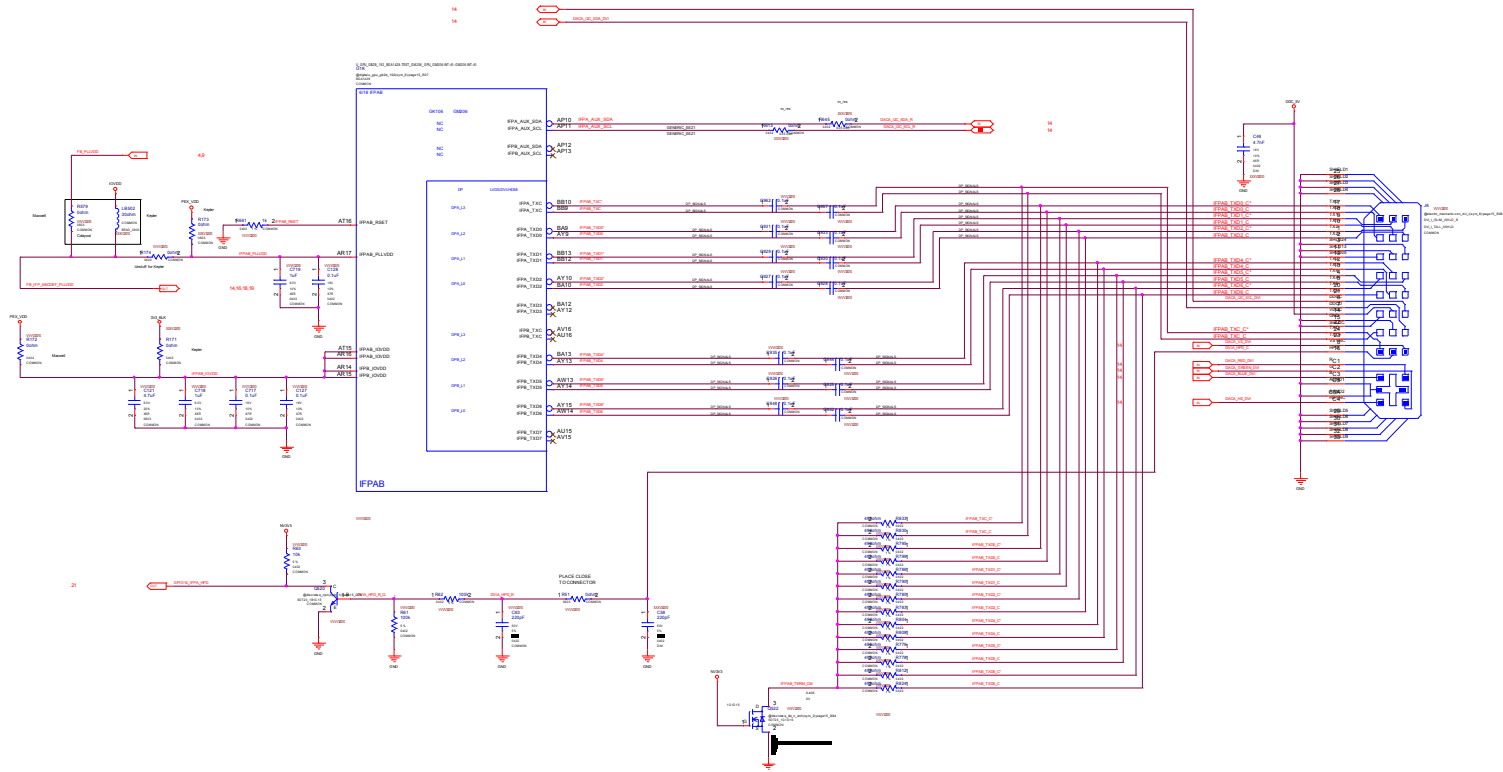
15 MSB/LSB

15 MSB/LSB

15 MSB/LSB

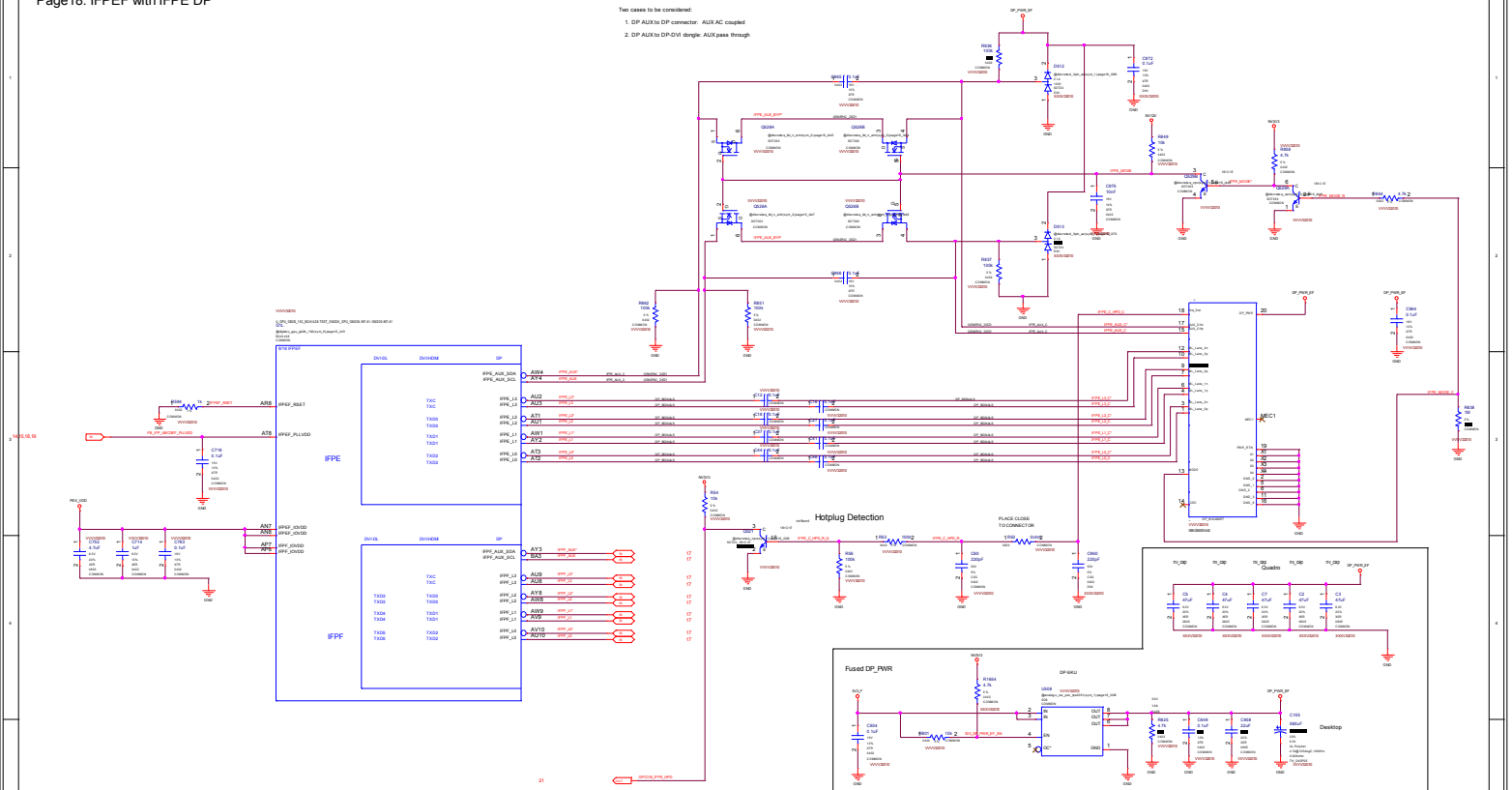
15 MSB/LSB

IFPAB DVI-I-DL

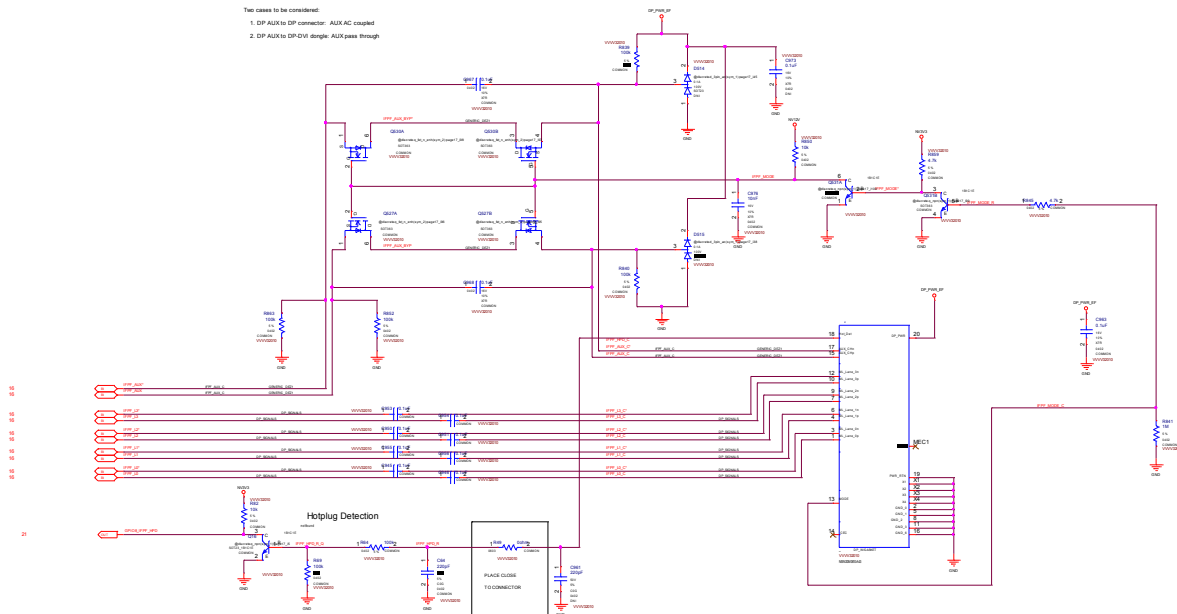


Two cases to be considered:

1. DP AUX to DP connector: AUX AC coupled
2. DP AUX to DP-DVI dongle: AUX pass through

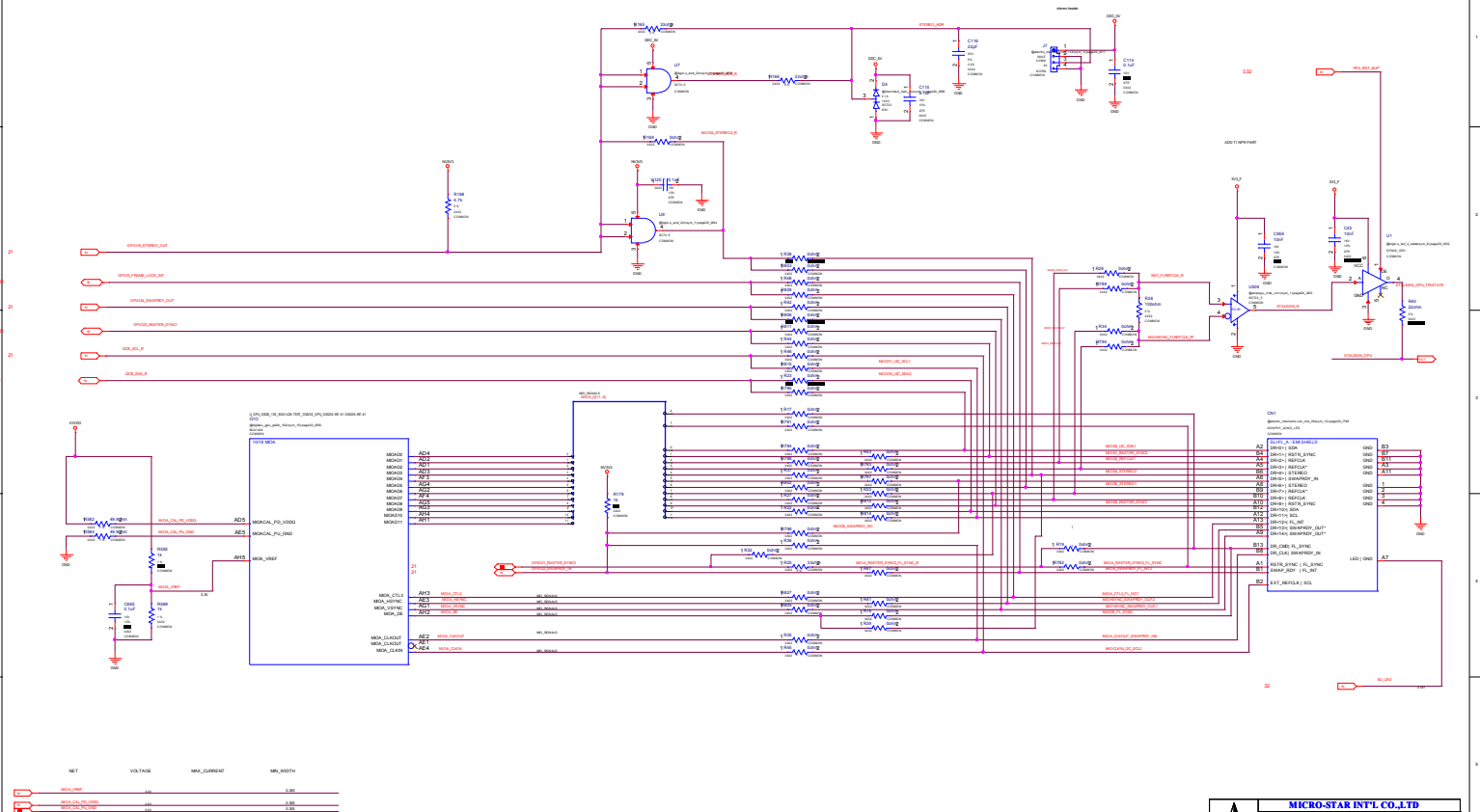


- Two cases to be considered:
1. DP AUX to DP connector: AUX AC coupled
 2. DP AUX to DP/DVI adapter: AUX pass through





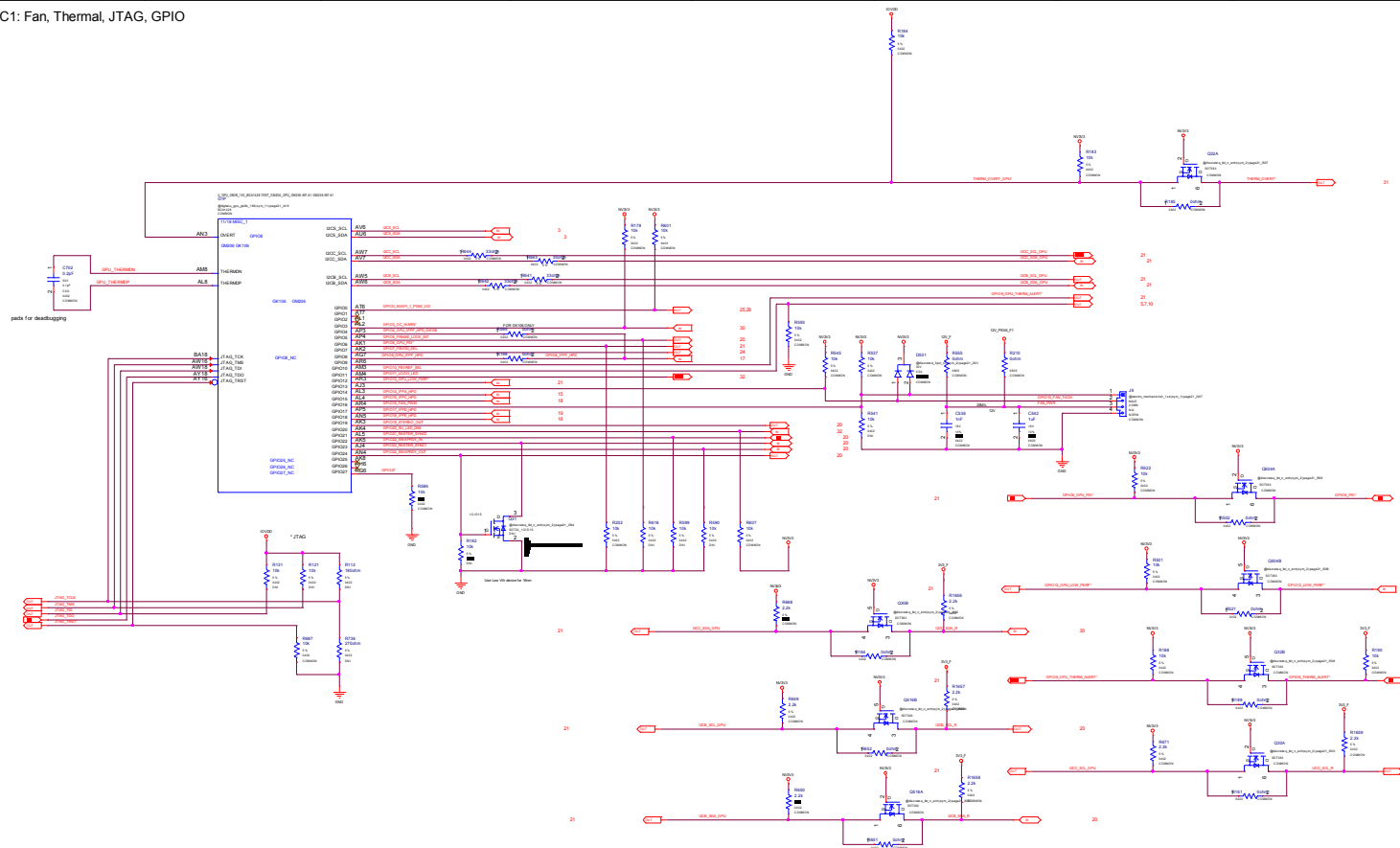
MIOA/SLI Interface



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	MS-V320		
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	Date: Wednesday, November 03, 2014 11:58:26 AM of 14		

MISC1: Fan, Thermal, JTAG, GPIO

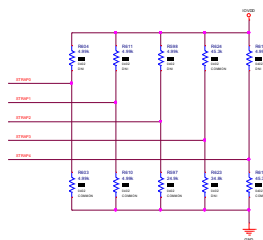


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MISC2: ROM, XTAL, Straps

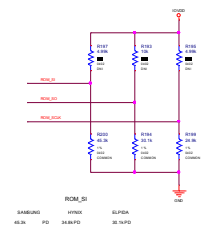
KEPLER		MAXWELL	
STRAP0	USER_BTN [0:0?]		
STRAP1	3IG0_PADCFO_LUT_ADR*		
STRAP5	PC0_DEVICE [0:0?]		
STRAP9	SOR1_EXPOSED [0:0?]		
STRAP6	DP_FL_L_VDD_33V*		
	PEX_MAX_SPEED?		
	PEX_SPD_CHANGE_GEN?		
	-		
ROM_B	RAMCFQ[0?]	RAMCFQ[0?]	
	RAMCFQ[1?]	RAMCFQ[1?]	
	RAMCFQ[2?]	RAMCFQ[2?]	
	RAMCFQ[3?]	RAMCFQ[3?]	
	VGA_DEVICE?	VGA_DEVICE?	
ROM_SO	SMB_ALT_ADDR*	SMB_ALT_ADDR*	
	FIBL1_APERTURE_SIZE*	POE_CFO*	
	FIBL1_APERTURE_SIZE*	DEV_D_SEL*	
	PEX_FL_EN_TERM00?	SOR0_EXPOSED?	
ROM_SCLK	PC0_DEVICE_EXT0?	SOR1_EXPOSED?	
	SUB_VEND0?	SOR2_EXPOSED?	
	PC0_DEVICE_EXT4?	SOR3_EXPOSED?	

MULTI-STRAP_RESET_SEQ	
RESET_PULSE_WIDTH	100
RESET_DELAY	100
RESET_FREQUENCY	100

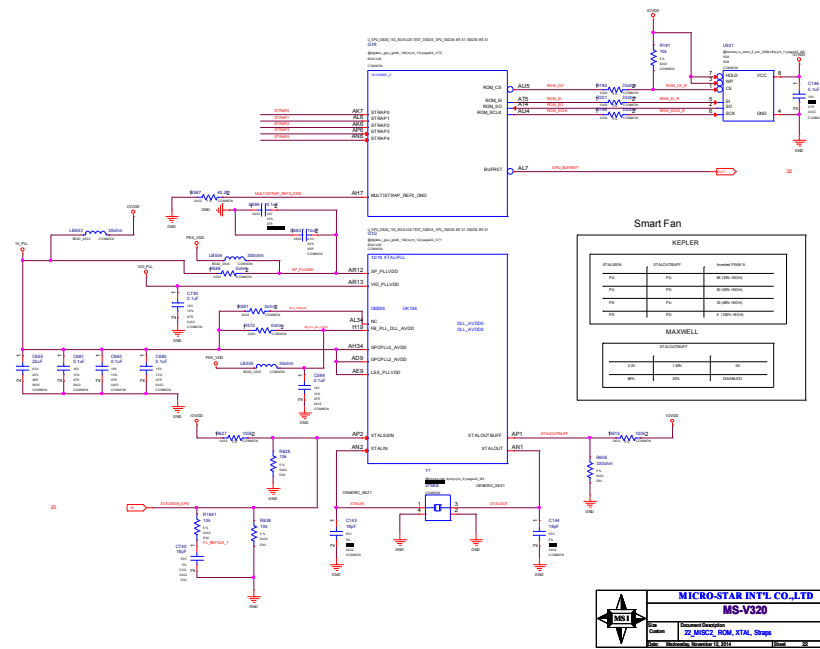


MAXWELL

STRAP	
STRAP0	1000
STRAP1	1000
STRAP2	1000
STRAP3	1000
STRAP4	1000
STRAP5	1000
STRAP6	1000
STRAP7	1000
STRAP8	1000
STRAP9	1000



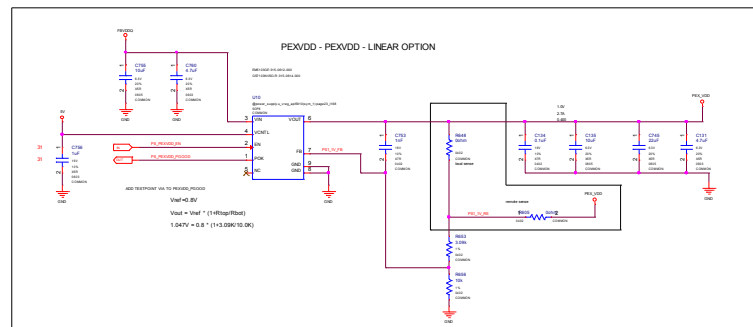
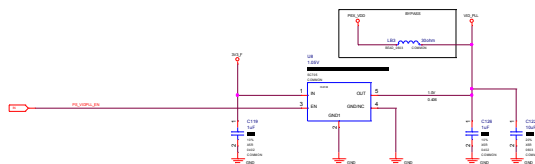
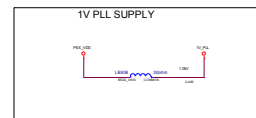
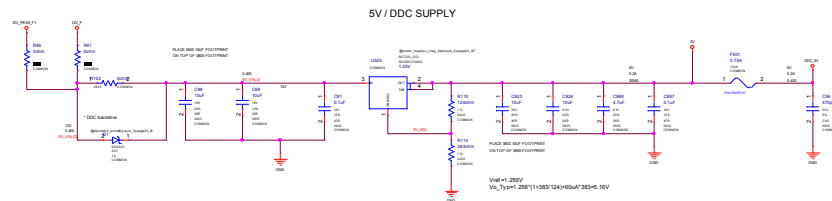
GND		VDD	
5A	0000	1000	
10A	0001	1001	
15A	0010	1010	
20A	0011	1011	
25A	0100	1100	
30A	0101	1101	
35A	0110	1110	
40A	0111	1111	



Smart Fan

KEPLER		MAXWELL	
STRAP0	1000	1000	
STRAP1	1001	1001	
STRAP2	1010	1010	
STRAP3	1011	1011	
STRAP4	1100	1100	
STRAP5	1101	1101	
STRAP6	1110	1110	
STRAP7	1111	1111	

PS: 5V, PEX_VDD



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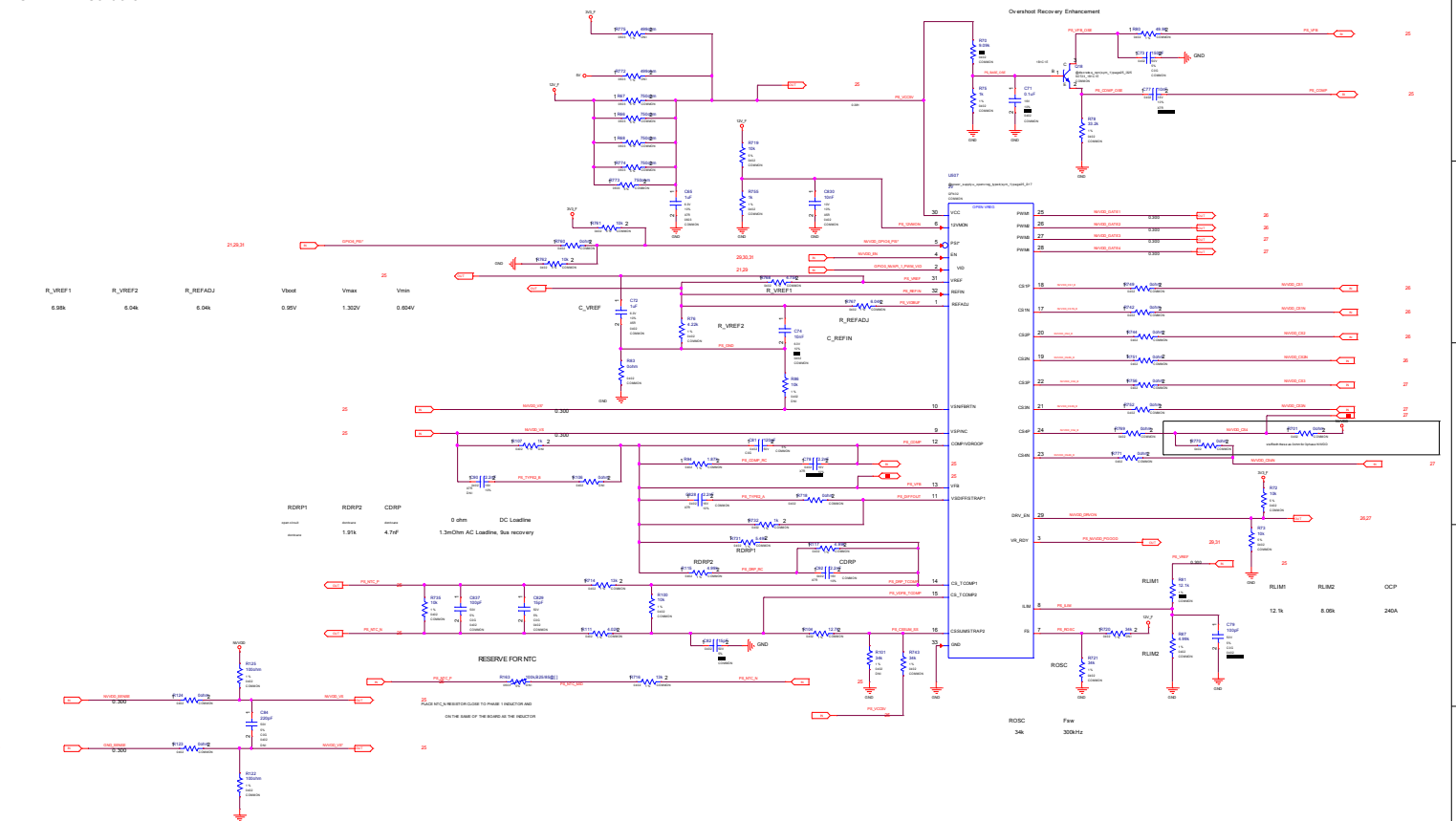
MS-V320

Size	Document Description
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File: Wednesday, November 12, 2014



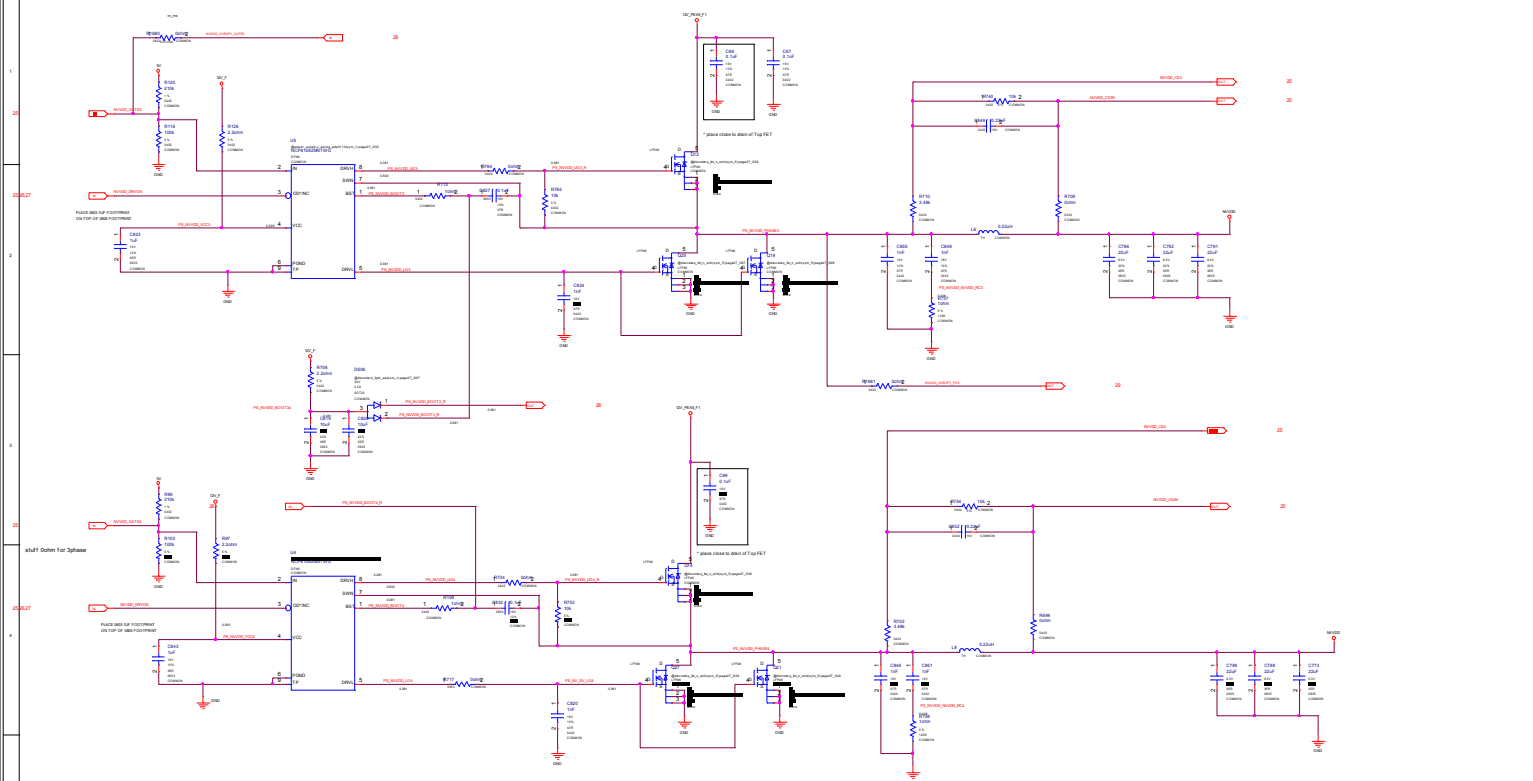
PS: NVVDD Controller



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PS: NVVDD Phase 3,4

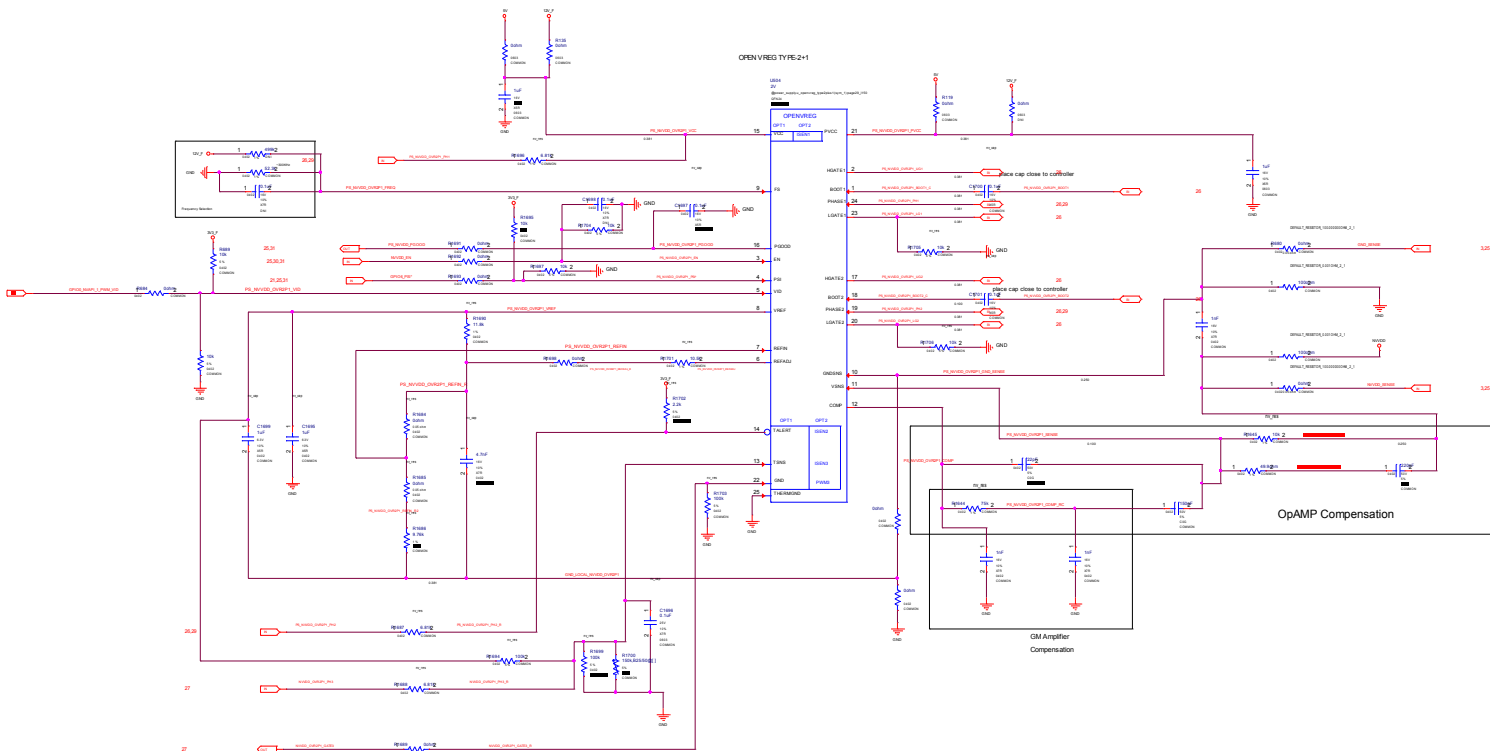


[RESERVED]

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Date: Wednesday, November 14, 2018		Sheet 38 of 41	

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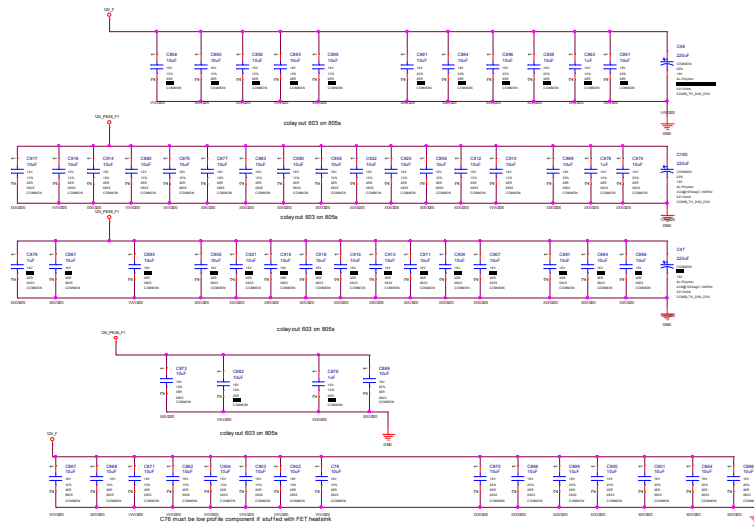
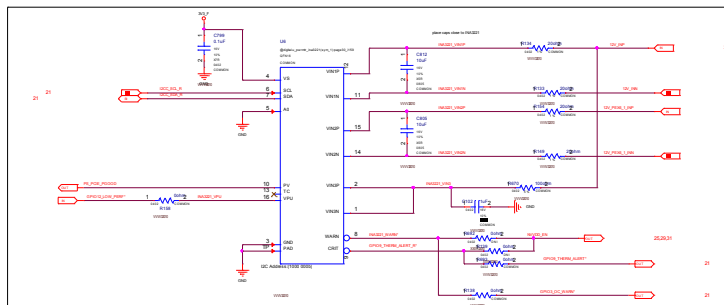
PS: NVVDD OVR2+1 POWER



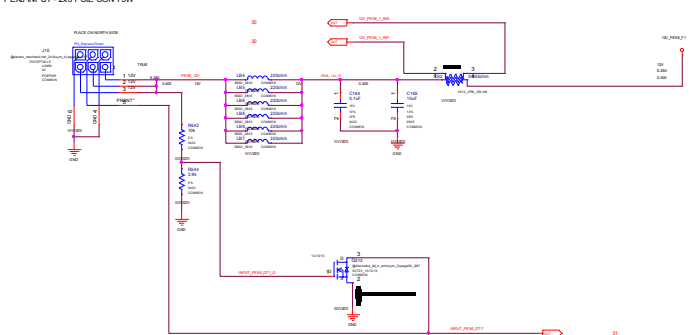
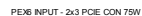
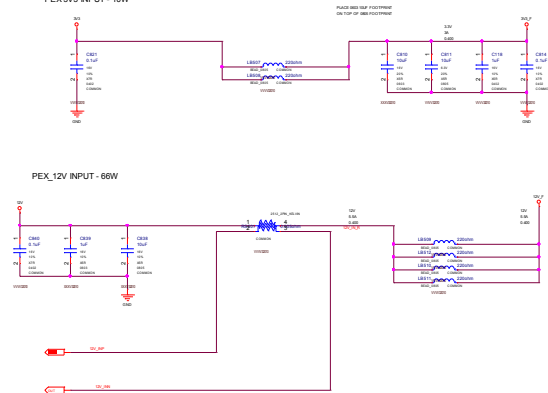
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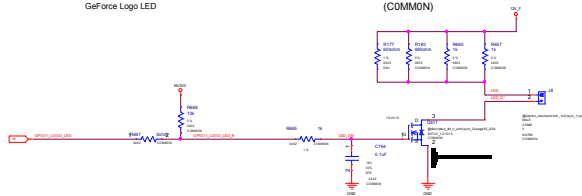
Size	Document Description
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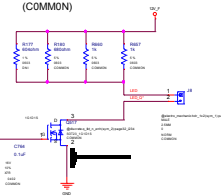


LEDs

GeForce Logo LED

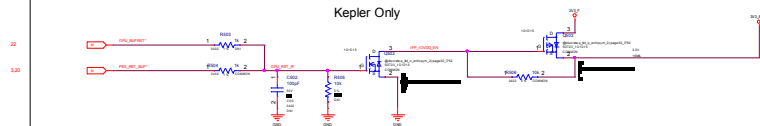


LED HEADER (COMMON)



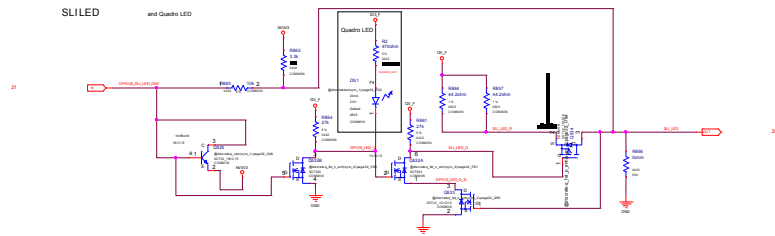
IFP_OVDD (backdrive prevention)

Kepler Only



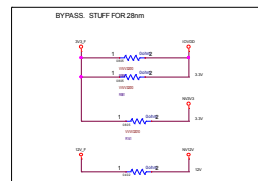
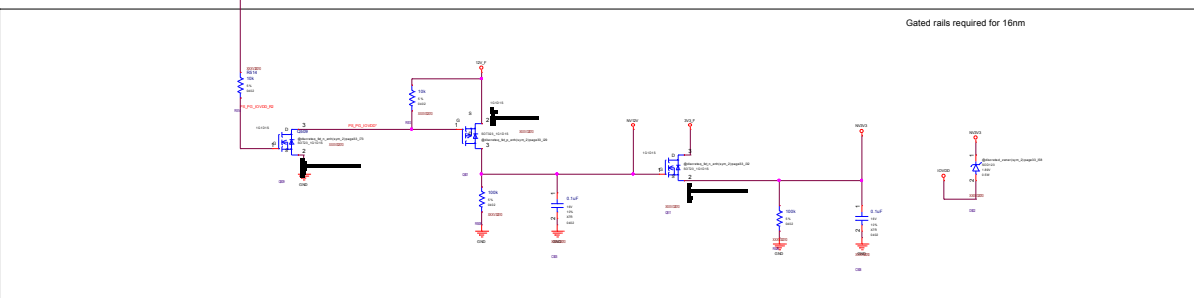
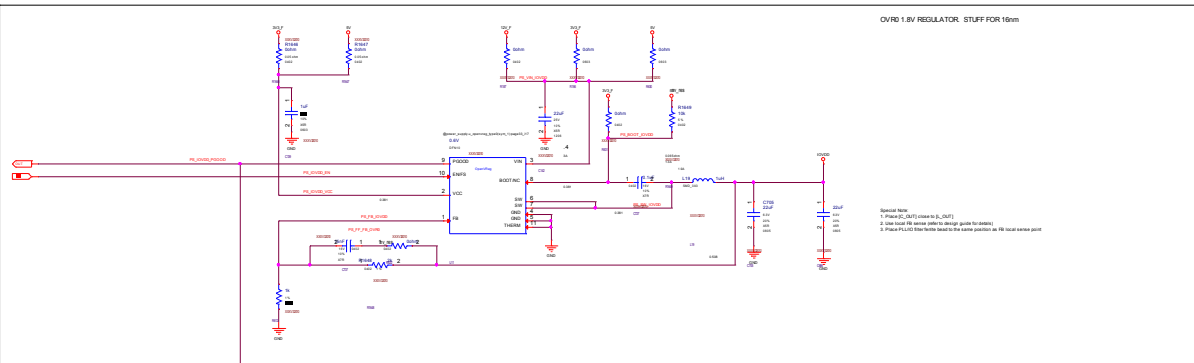
SLI LED

and Quadro LED



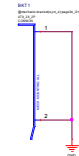
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Rev	0.1
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Date	2010.01.12
Ver	1.0



Mechanical: Bracket/Thermal Solution

Brackets:



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